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438	239
Class	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

 I.P.E.	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/961264	D F	257438	239	2811 2822	DAVOG T. Fahmy

APPLICANTS

Narumi Ohkawa

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Semiconductor device having both memory and logic circuit and its manufacture

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheet No. <u>12</u>	Figs. Drawn <u>27</u>	Print Figs. <u>1A-1C</u>	Total Claims <u>10</u>	Patent Claims for U.S. <u>4</u>
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(FACE)